

For Immediate Release

TrellisWare Introduces New RapidFire Development Platform Employing Altera's Stratix II FPGAs

San Diego, California, October 29, 2007 - TrellisWare Technologies, Inc., an innovator in advanced communications processing, today unveiled its **RapidFire** hardware accelerator/development board at the MILCOM 2007 Military Communications Conference in Orlando, Florida. Enabling rapid prototyping and testing of complex Physical Layer designs, RapidFire hosts two Altera Stratix II FPGAs and provides a large number of logic gates and RAM blocks for hardware acceleration of even the most stressing signal processing architectures.

The RapidFire development board was designed to enable evaluation of TrellisWare's full range of waveform, high speed modem and FEC IP products, including the latest **Chameleon** Flexible Low Density Parity Check (F-LDPC) ASIC, which is pin-compatible with the Altera Stratix II FPGA.

To showcase its capabilities, TrellisWare will be demonstrating the F-LDPC implementation in Chameleon by encoding and decoding a very high throughput data stream with injected noise. Error curves can be generated for any combination of a large number of code rates and block sizes.

"RapidFire is a major breakthrough for communication system designers," said Metin Bayram, director of FEC and high speed modem solutions at TrellisWare. "Compared to a C-simulation environment, RapidFire can produce in seconds performance results that would typically take days or weeks."

TrellisWare will be featuring RapidFire and other F-LDPC products in Altera's booth at the MILCOM conference (Booth # 1430). RapidFire is available for immediate shipment to customers.

About TrellisWare

TrellisWare Technologies, Inc. is a privately-held communications IP and products company headquartered in San Diego. Self funded since its incorporation in April 2000, TrellisWare has built a reputation as a leader in advanced communication algorithms, waveforms and turnkey communication systems that work when nothing else does. TrellisWare has developed a wide range of highly-advanced Forward Error Correction (FEC) algorithms and software defined radio (SDR) waveforms used in many military and commercial communication products. With deep expertise in radio physical layer design, networking, efficient high speed decoding, algorithm development and RF integration, TrellisWare is also developing a unique family of communication products capable of operating in the harshest RF environments.